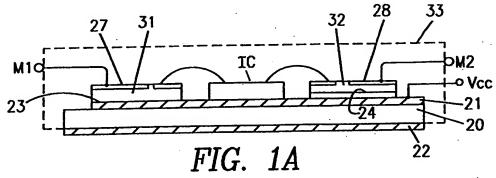


FIG. 1



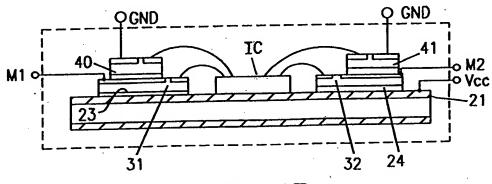


FIG. 1B

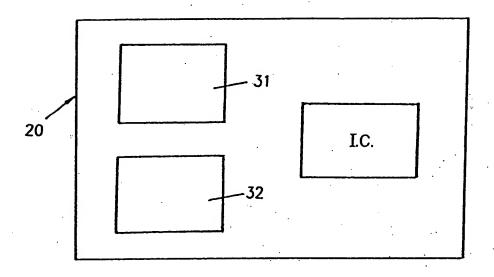


FIG. 1C

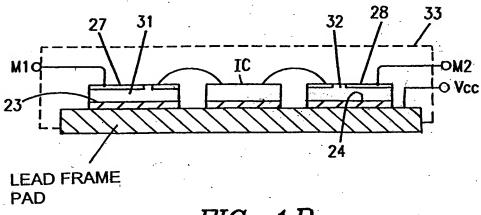
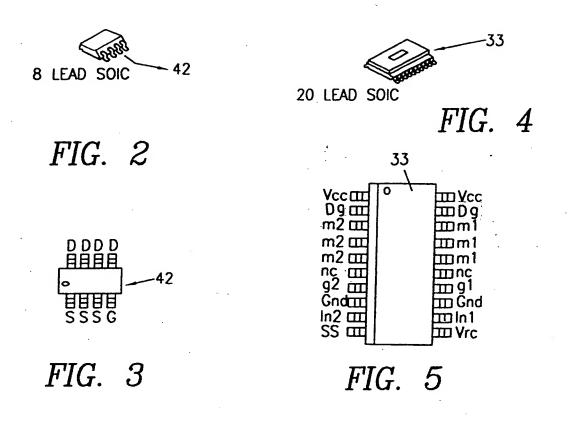


FIG. 1D



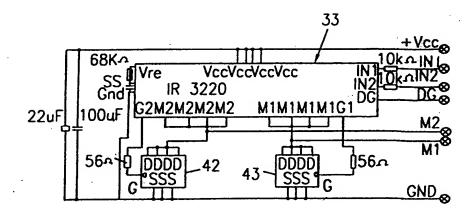
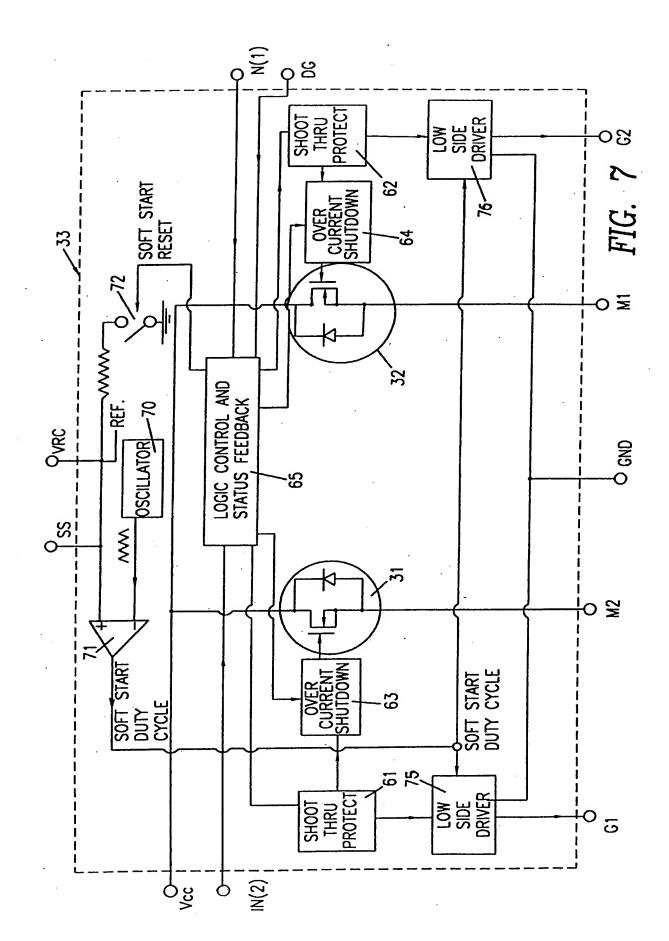
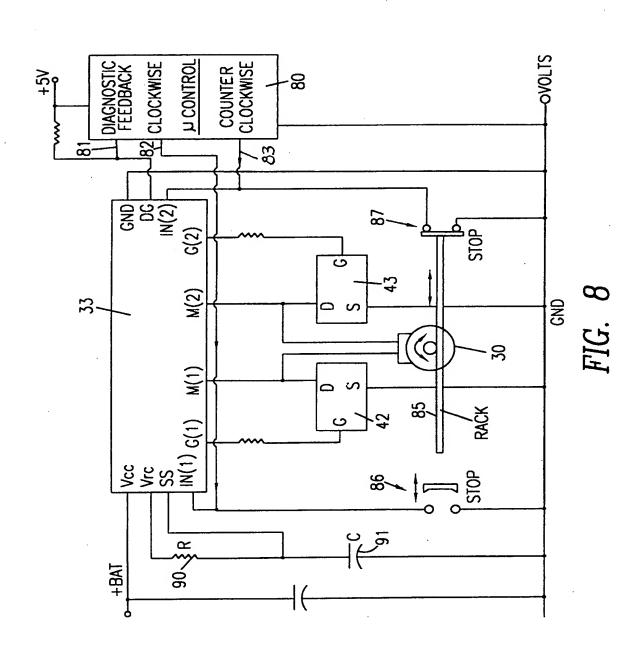
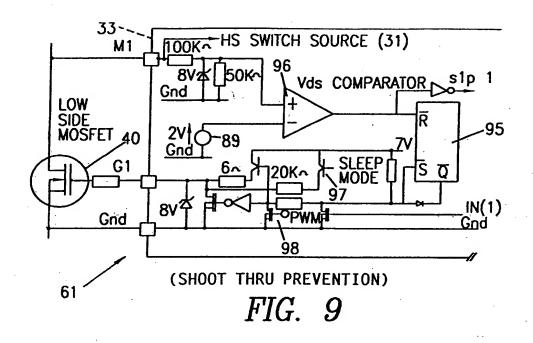


FIG. 6







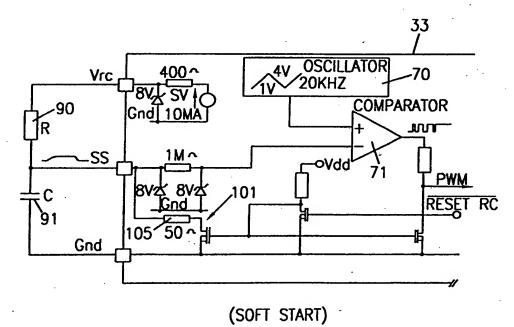


FIG. 10

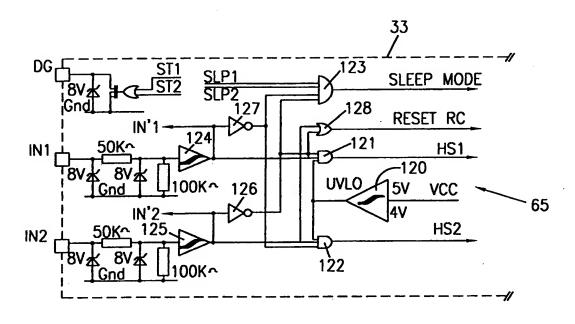
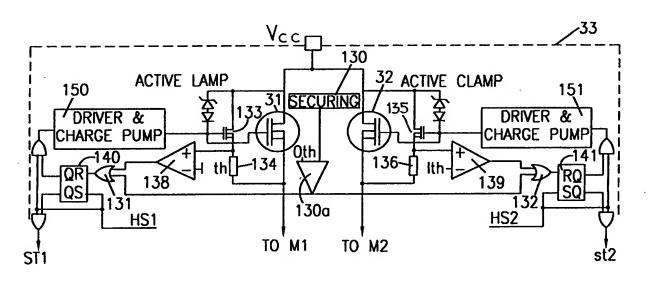


FIG. 11



shut down circuits FIG.~12

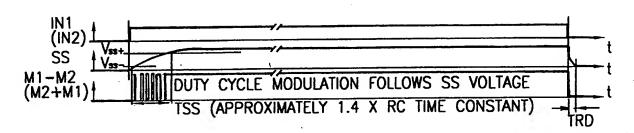


FIG. 13

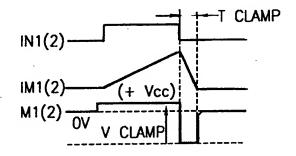


FIG. 14

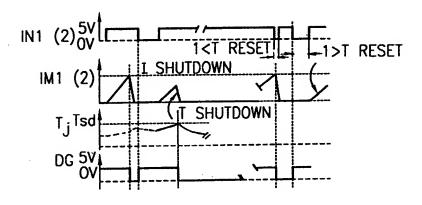


FIG. 15

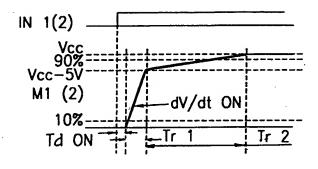


FIG. 16

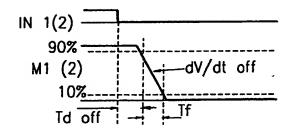


FIG. 17

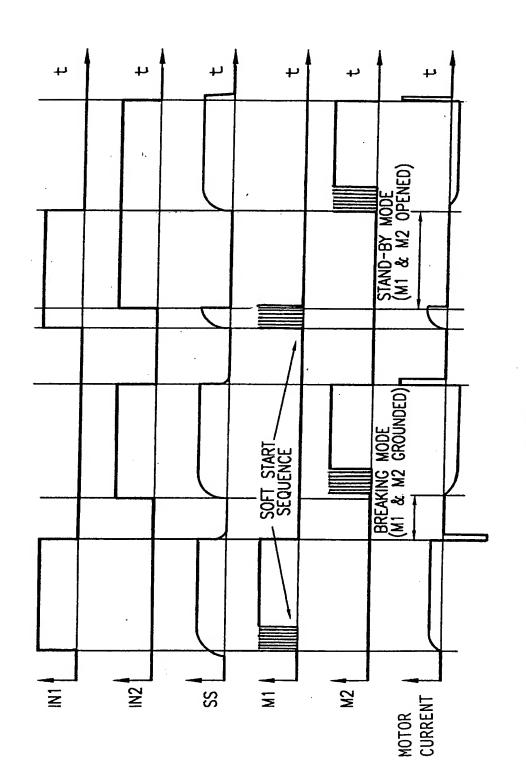


FIG. 18